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**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

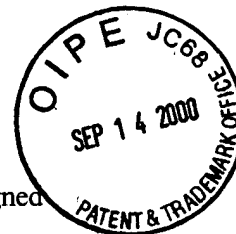
Atsushi SAKAI, et al.

Appln. No. 09/576,957

Filed: May 24, 2000

Group Art Unit: Unassigned

Examiner: Unassigned



For: SOLID ELECTROLYTIC CAPACITOR AND METHOD FOR PRODUCING THE SAME

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. §§ 1.97 and 1.98**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached Form PTO-1449 and/or listed herein and which the Examiner may deem relevant to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed (1) no later than three months from the application's filing date or (2) before the mailing date of the first Office Action on the merits (whichever is later), and therefore no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

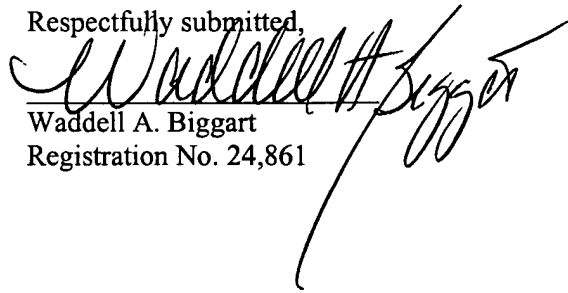
The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to

INFORMATION DISCLOSURE STATEMENT  
U.S. Appln. No. 09/576,957

take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

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Respectfully submitted,

  
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Date: September 14, 2000